CURRICULUM VITAE

Name:	DATTATRAY MAHADEO KUMBHAR
Designation:	Assistant Professor
Department:	Electronics & Communication Engineering
Specialization & Research Interests:	Digital Electronics
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2. ACADEMIC QUALIFICATION (in reverse Chronological order):

Degree	Year	University/Board
M.Tech.	2015	VTU Belgaum
B.E.	2000	Shivaji University Kolhapur

3.   ANY OTHER QUALIFICATION:

4.   PROFESSIONAL EXPERIENCE:

Organization/Institute/University	Position Held	Duration
Renutron Power Corporation , Kolhapur	Senior Engineer	01/06/2000 to 01/08/2007
Hirasugar Institute of Technology, Nidasoshi	Lecturer	02-08-2007 to 01-02-2010
Hirasugar Institute of Technology, Nidasoshi	Sr. Lecturer	02-02-2010 to 31-03-2016
Hirasugar Institute of Technology, Nidasoshi	Asst. Professor	01-04-2016 to till date

5.   ADMINISTRATIVE ASSIGNMENTS:

Position Held	Duration	Nature of Work
Senior Engineer	01/01/2005 to 01/08/2007	Testing engineering and overall monitoring of production

6.   COURSES TAUGHT:

i.   Basic electronics
ii.   Analog electronics
iii.   Logic Design
iv.   Electronic Instrumentation
v.   Network theory
vi.   Linear IC's & applications
vii.   Information theory and coding
viii. Basics of CMOS,
ix.   Management & Entrepreneurship
x.   Digital system design using Verilog,
xi.   Satellite communication
xii.   VLSI Design
xiii. Power Electronics
xiv.   Embedded system
xv.   Sensors & signal conditioning
xvi.   Innovation and Design Thinking.

7. RESEARCH SUPERVISION:

A. Ph.D:

i. Awarded   :

ii. Submitted   :

iii. Ongoing   :

B: M.Tech.

i. Awarded   :

ii. Submitted   :

iii. Ongoing   :

C. BE/B.Tech Degree Oriented (Bachelor Level Dissertation):

i. Awarded   :16

ii. Submitted   :16

iii. Ongoing   :01

8. CONTRIBUTION TO CORPORATE LIFE OF THE INSTITUTE: NIL

9. MEMBERSHIP OF SOCIETIES/PROFESSIONAL BODIES:

- ISTE—LM 62343

10. PUBLICATIONS:

A. BOOKS/MONOGRAPHS:

1. Authored:

i .   --

2. Edited:

i.   --

B. PAPERS IN REFEREED/PEER REVIEWED JOURNALS:

      i.   --

C. **PAPERS IN CONFERENCES PROCEEDINGS:**

i. **"Boosting Input Voltage And Improving PF using PFC Circuit"** published in Volume 4, Issue 1November 2013   by **IJNTEC**

ii. **"Maximum power point tracking using Fuzzy Logic controller"** published paper in Journal of advances in Science and Technology Vol. 12, Issue No. 25, Dec. 2016 ISSN 2230-9659.

iii. **"Maximum power point tracking using Fuzzy Logic controller"** presented paper in the international conference on recent innovations in engineering (iCRiE-16) held at Hirasugar Institute of Technology, Nidasoshi.

iv. **"Agriculture Robot"** published paper in Journal of Emerging Technologies and Innovative Research (JETIR) Volume 5, Issue 9, September 2018, ISSN-2349-5162.

v. **"Comparison of DC to DC Converter for PV System to Track MPP"** published paper in Journal of Emerging Technologies and Innovative Research Volume 6 Issue 6, June 2019, ISSN-2349-5162.

vi. **"Smart Floor Cleaning Robot"** published paper in Journal of Emerging Technologies and Innovative Research Volume 7 Issue 10, October 2020, ISSN-2349-5162.

vii. **"A smart solution for in farm complete tractor activity tracking system over cloud"** published paper in Journal of Emerging Technologies and Innovative Research Volume 8 Issue 10, October 2021, ISSN-2349-5162.

11.   Patents/Copyrights/IPR (If Any)

12.   INVITED TALKS:

13.   RESEARCH PROJECTS *(COMPLETED/ONGOING):*

14.   PARTICIPATION & PRESENTATIONS IN SEMINARS /SYMPOSIA/ WORKSHOPS/ CONFERENCES:

i. Analog and Mixed Mode VLSI Design
ii. CMOS VLSI & ASIC DESIGN using MICROWIND tool
iii. Application of Mathematics in Engineering
iv. Signals And Systems
v. Overview of arm Microcontroller & Its Applications
vi. Network Simulator 2 for wired Wireless Network

vii.   Internet Of Things
viii. Internet of Things and its real time applications
 ix.   "Digital circuits" Twelve week NPTEL online certification
  x.   Six days online FDP on "Battery Management and Control Techniques" in Electric Vehicles
 xi.   Machine Learning and Data Analytics
 xii. Workshop on "Design and Simulation of Microstrip Antenna using HFSS tool"
xiii. Two days online FDP on "Skill Enhancement for Academicians"
xiv.   Four days FDP on "Software Engineering and Testing Methodologies"

15.   AWARDS, FELLOWSHIPS & OTHER DISTINCTIONS:

1. KSCST Sanction of Student Project - 42th Series: Year 2018-2019 amount Rs. 5000/-
2. KSCST Sanction of Student Project - 43th Series: Year 2019-2020 amount Rs. 5000/-
3. KSCST Sanction of Student Project - 44th Series: Year 2020-2021 amount Rs. 5000/-

16.   ANY OTHER SIGNIFICANT INFORMATION:

None

(Prof. D M Kumbhar)