



CURRICULUM VITAE

Name:	Prof.PRAMOD V PATIL	
Designation:	Assistant Professor	
Department:	Electronics & Communication Engineering.	
Specialization & Research Interests:	VLSI Design & Embedded Systems	
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2. ACADEMIC QUALIFICATION (in reverse Chronological order):

Degree	Year	University/Board
M. Tech	2012	SJCE, Mysuru
B.E.	2009	HSIT Nidasoshi

3. ANY OTHER QUALIFICATION:

NONE

4. PROFESSIONAL EXPERIENCE:

Organization/Institute/University	Position Held	Duration
Hirasugar Institute of Technology, Nidasoshi	Assistant Professor	06-08-2012 to Till Now

5. ADMINISTRATIVE ASSIGNMENTS:

Position Held	Duration	Nature of Work
Department EMS & IA Coordinator	2012 to 2015	Examination section management at Department level
Department ISTE Coordinator	2013 to 2017	Coordinating the College ISTE Chapter from ECE Department
Department TP Cell Coordinator	2017 to Till Date	Coordinating the Training & Placement Activities from ECE Department
Department ALUMNI Coordinator	2015 to Till Date	Coordinating the Alumni Activities from ECE Department
Department NBA Criteria 6 Head	2015 to Till Date	Handling all the tasks of Criteria 6 of the NBA
E-Yantra Lab Incharge	2017 to Till date	Handling E-Yantra Lab

6. COURSES TAUGHT:

1. Real Time Systems.
2. Real Time Operating Systems.
3. Embedded Systems.
4. Information Theory & Coding.
5. Operating Systems.
6. Microprocessor.
7. Microcontroller.
8. Network Analysis.
9. Programming in C++.
7. Python Application programming.
8. Verilog HDL
9. Machine Learning using Python.

7. RESEARCH SUPERVISION:**A. Ph.D:**

- i. Awarded : None
- ii. Submitted : None
- iii. Ongoing : None

B: M.Tech:

- i. Awarded : None
- ii. Submitted : None
- iii. Ongoing : None

C. BE/B.Tech Degree Oriented (Bachelor Level Dissertation):

- i. Awarded : 10
- ii. Submitted : 10
- iii. Ongoing : 01

8. CONTRIBUTION TO CORPORATE LIFE OF THE INSTITUTE:

1. Referred many students to take the admission in our college as well as in the Sister institutions.
2. Referred a Candidate when requirement has been raised in Civil department.

9. MEMBERSHIP OF SOCIETIES/PROFESSIONAL BODIES:

1. Life Member of Indian Society for Technical Education.
2. Ignited Minds Professional & Academic Research Consortium.

10. PUBLICATIONS:**A. BOOKS/MONOGRAPHS:**

1. Authored:
 - i. None
 - ii. None
 - iii. None
2. Edited:
 - i. None
 - ii. None
 - iii. None

B. PAPERS IN REFEREED/PEER REVIEWED JOURNALS:

- i. None
- ii. None
- iii. None

C. PAPERS IN CONFERENCES PROCEEDINGS:

i. **P.V.PATIL** , N.R Patil, P.R Jadhav , “**Implementation Of RISC Processor using Verilog**” in Journals of Advances in Science & Technology Vol.12, Issue No. 25(Special Issue) December-2016, ISSN 2230-9659.

ii. **P.V.Patil** , T.V.Hegade , A.K.Lele , “**Implementation of Soft Error-Resilient built in 2D Hamming Product code using Verilog**” in Journals of Advances in Science & Technology Vol.12, Issue No. 25(Special Issue) December-2016, ISSN 2230-9659.

iii. N. M. Patel , **P. V. Patil** , G. M. Alan, P. B. Done, “**Design of pipelined RISC MIPS processor (16 - Bit) using VLSI technology,**” International Journal of Emerging Technologies and Innovative Research, vol.5, issue 9, pp. 143-152, September 2018.

iv. D. B. Madihalli, D. M. Kumbhar, **P. V. Patil** and S. S. Ittannavar 05 Prof. D. M. Kumbhar, “**Agricultural Robot,**” International Journal of Emerging Prof. P. V. Patil & Prof. Technologies and Innovative Research” vol.5, issue 9 , pp. 292– S. S. Ittannavar 294, September 2018

v. M.N.Shanmughaswamy, Cyril.Prasanna.Raj , **P.V.Patil**, “**A multilayered Perceptron reconfigurable ANN (Artificial Neural Networks) For image compression & Decompression**” in National Level Conference on Recent Advancement in Engineering (NCRAE-12) at SETI, Panhala Kolhapur.

vi. A. R. Patil , **P. V. Patil** , Rajeshwari Khot, Laxmi Patil and Anusha Fernandes “**Implementation of data encryption standard algorithm using verilog,**” International Journal of Emerging Technologies 06 Prof. P. V. Patil and Innovative Research, vol.5, issue 8 , pp.734–746, August 2018.

vii. **P.V.Patil** , Mohan Basapuri, “**ARUINO UNO based system to develop the smart Village**” 2nd International Conference on Recent Innovations in Science, Engineering & Technology (ICRISET-19).

11. Patents/Copyrights/IPR (If Any)

Innovation Patent at Australian Government (IP Australia Patent Act 1990).

Title of invention: Deep learning based real time smart detection of accident victims on highways.

Patent number: 2021101684

Term of Patent: Eight years from 1 April 2021

12. INVITED TALKS:

None

13. RESEARCH PROJECTS (COMPLETED/ONGOING):

Deep learning based real time smart detection of accident victims on highways.

**14. PARTICIPATION & PRESENTATIONS IN SEMINARS /SYMPOSIA/
WORKSHOPS/ CONFERENCES:**

1. National Level Workshop on Embedded Linux Multimedia & Android Application in the duration 23/03/ 2013 to 24/03/ 2013 at HSIT, Nidasoshi.
2. IETE Approved Three days National Level Workshop on Network Simulator for Wired Wireless Network in the duration 17/10/2013 to 17/10/2013 at SIT polytechnic, Yadrav Inchalkarnji
3. Workshop on Professional Android Application Development in the duration 15/03/ 2014 To 16/03/ 2014 at HSIT, Nidasoshi
4. NASSCOM foundation in Software & IT at STJIT Ranebennur in the duration 01/01/2015 To 03/01/2015 STJIT Ranebennur.
5. FDP on VLSI Design using Cadence Tools in Collaboration with Entuple in the duration 29/06/ 2015 to 03/07/ 2015 at KSIT Bangalore.
6. ICRIE-2016 Conference at HIT Nidasoshi. in the duration 08/09/16 to 09/09/16 at HSIT, Nidasoshi.
7. Two-week ISTE STTP on CMOS,MIXED Signal and Radio frequency VLSI Design, by IIT Kharagpur in the duration 26/12/16 to 04/02/16 Gogte Institute of Technology, Belgaum
8. FDP on "Internet of things & it's Real time Applications " Organized by the E&ICT Academy ,NIT Warangal in the duration 28/01/ 2019 to 02/02/ 2019 HSIT, Nidasoshi
9. National Energy Conservation Week in Association with Karnataka renewable Energy Development Ltd. in the duration 20/12/ 2019 to 20/12/ 2019 HSIT, Nidasoshi
10. Machine Learning and Data Analytics Sponsored by VGST,Govt of Karnataka and CSE Dept HSIT Nidasoshi in the duration 08/01/ 2020 to 11/01/ 2020 HSIT, Nidasoshi
11. AICTE sponsored Two –weeks Faculty Development programme on "Current Practices and Critical Issues for OBE in Engineering Education/Institution " in the duration 27/01/2020 07/02/2020 at BEC, Bagalkot.
12. Design & Simulation of Microstrip Antenna using HFSS Tool conducted by Niha Solutions,Bengaluru in the duration 26/02/2020 to 29/02/2020 at HSIT, Nidasoshi.
13. A Webinar on "Measurement Technology-Industrial Sensors" by SJBIT,Bengaluru. On 24/05/2020 at Bengaluru.
14. A Three Days Webinar on "Arduino Programming & IOT" by DKTE Inchalarnji. in the duration 25/05/2020 to 27/05/2020 at Inchalarnji.
15. A Webinar on "Design Of Analog Layout & Opportunities & Challenges in semiconductor Domain" by KLCET Chikkodi. . in the duration 01/06/2020 to 02/06/2020 at Chikkodi.
16. A 3 days FDP on "Recent Trends in wireless sensor Networks & Communication Systems" by VSMIT Nippani. in the duration 03/06/2020 to 05/06/2020 at Nippani.
17. A Two Days National Level online FDP on "SKILLS ENHANCEMENT for Academicians" by JCI Engineering in the duration 08/06/2020 to 09/06/2020 at Belagavi.

18. Webinar on “Modeling Electrical Systems using Simulink” conducted by Department of ECE New Horizon College of Engg. In Association with CoreEL Technology. . in the duration 12/06/2020 to 12/06/2020 at Bengaluru.
19. One day online webinar on “CAD Tools for VLSI Design” by VSMIT Nippani in Collaboration with Trident Tech Labs Bengaluru in the duration 13/06/2020 to 13/06/2020 at Nippani.
20. A Five Day National Level online FDP on “Image & Video Processing Techniques using Python programming ” by New Horizon College Engineering in the duration 15/06/2020 To 19/06/2020 at Bengaluru
21. Webinar on Career After Covid-19 conducted by Department of ECE HSIT in the duration 21/06/2020 to 21/06/2020.
22. One Day National Level Webinar on “An introduction & overview of AWS cloud computing services” by Vemana Institute of Technology in the duration 24/06/2020 to 24/06/2020 at Bengaluru
23. One Day National Level Webinar on “MEMS based sensors for chemical & Biomedical sensing application” by Sri Vasvi Engineering College in the duration 15/06/2020 to 15/06/2020.
25. A Five Day National Level online FDP on “Trends in Electronics & Communication Engineering” by Sri Vasvi Engineering College in the duration 22/06/2020 to 26/06/2020 at Tadepalligudem, Andhra Pradesh.
26. Webinar on How to crack gate in first attempt conducted by Department of ECE HSIT on 28/06/2020 at HSIT, Nidasoshi.
27. Webinar on Education Abroad conducted by Department of ECE HSIT on 06/07/2020 At HSIT Nidasoshi.

15. AWARDS, FELLOWSHIPS & OTHER DISTINCTIONS:

1. Best Paper Award in “International Conference on Recent innovations in Science, Engineering & Technology” ICRASET-18 for the paper Implementation of data Encryption standard algorithm.
2. Got Sanctioned Rs 81,000/- on 08/09/16 for E-Mantra Lab, IIT Bombay Obtained 3 Firebird V -2560 bots.
3. Got Sanctioned Rs 3500/- on 27/03/17 for Design and Implementation of 16 point FFT Processor for OFDM using HDL from KSCST Bangalore.
4. Got Sanctioned Rs 3500/- on 27/03/17 for Design and Implementation of 16 point FFT Processor for OFDM using HDL from KSCST Bangalore.
5. Best Project award in 43rd Student Project Programme of KSCST winning an Amount of 11000/-

16. ANY OTHER SIGNIFICANT INFORMATION:

None

Prof. PRAMOD.V.PATIL