



CURRICULUM VITAE

Name:	Smt. Purima I Savadatti	
Designation:	Assistant Professor	
Department:	Electrical and Electronics Engineering	
Specialization & Research Interests:	Digital Electronics	
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Address:	Mahaveer Colony, Jayanagar At/Post: Hukkeri Taluk: Hukkeri District: Belagavi Karnataka - 591239	

2. ACADEMIC QUALIFICATION(in reverse Chronological order):

Degree	Year	University/Board
Ph.D.	-	-
M.Tech.	2016	VTU, Belagavi
B.E.	2012	VTU, Belagavi

3. ANY OTHER QUALIFICATION:

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4. PROFESSIONAL EXPERIENCE:

Organization/Institute/University	Position Held	Duration
Shree Ulavi Channabasaveshawar Polytechnic, Dandeli.	Lecturer	July 2012 to Nov 2016
Dr A.D.Shinde College of Engineering, Gadhinglaj.	Asst. Professor	Jan 2018 to March 2021
Hirasugar Institute of Technology, Nidasoshi	Asst. Professor	Dec 2022 to till date

5. ADMINISTRATIVE ASSIGNMENTS:

Position Held	Duration	Nature of Work
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6. COURSES TAUGHT:

- DC Machines
- HVAC
- Mechatronics
- Transformers
- Electrical Instruments

7. RESEARCH SUPERVISION:**A. Ph.D.:**

- i. Awarded : ---
- ii. Submitted : ---
- iii. Ongoing : ---

B: M.Tech.

- i. Awarded : ---
- ii. Submitted : ---
- iii. Ongoing : ---

C. BE/B.Tech Degree Oriented (Bachelor Level Dissertation):

- i. Awarded : 08
- ii. Submitted :
- iii. Ongoing :--

8.CONTRIBUTION TO CORPORATE LIFE OF THE INSTITUTE:

9. MEMBERSHIP OF SOCIETIES/PROFESSIONAL BODIES:

10. PUBLICATIONS:**A. BOOKS/MONOGRAPHS:**

- 1. Authored:

- 2. Edited

B. PAPERS IN REFEREED/PEER REVIEWED JOURNALS:

- 1 Published paper entitled "**Load Equalizer Transformer**" in a Journal of Composition Theory, Volume 12, Issue 6, June 2019, UGC Approved & 5.7 Impact Factor, ISSN 0731-6755
- 2 Paper entitled "**VLSI implementation of high speed FFT processor**" in Journal of advances in science and Technology, ISSN:2230-9659, Volume-12, issue-25(special issue), Dec 2016.
- 3 Paper Entitled "**Ethernet Controller as solution for Embedded Applications, New Developments and Innovations**" Published in IOSR Journal of Electronics and Communication Engineering (ISSN: 2278-2834, ISBN 2278-8735, PP 39-43).

C.PAPERS IN CONFERENCES PROCEEDINGS:

1. Presented paper entitled “**Load Equalizer Transformer**” in 2nd International Conference on ‘**Recent Innovations in Science, Engineering and Technology**’ on 18th July 2019 held at HIT, Nidasoshi, Karnataka
2. Presented paper entitled “**VLSI implementation of high speed FFT processor**” in International conference on Recent innovations in engineering and science on 8th and 9th Oct 2016 at Hirasugar institute of technology, Nidasoshi.
3. Presented a Paper Entitled “**Ethernet Controller as solution for Embedded Applications, New Developments and Innovations**” in 2nd International conference on Emerging Trends in Engineering (ICETE-2013) held on 22nd and 23rd Feb 2013 at Dr. J.J Magadum college of Engineering, Jayasingpur.
4. Presented a Paper Entitled “**Optical Fibers**” in Intercollegiate technical paper presentation contest “**Aavishkaar**” organized by VDRIT, Haliyal held on 28th and 29th April 2009.

11. Patents/Copyrights/IPR (If Any)

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12. INVITED TALKS:

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13. RESEARCH PROJECTS (COMPLETED/ONGOING):-**14. PARTICIPATION & PRESENTATIONS IN SEMINARS / SYMPOSIA / WORKSHOPS / CONFERENCES:**

Sl. No.	Topic	Period		Venue
		From	To	
01	AICTE recognized short term course on “Student Evaluation Module-III: IPD through ICT” conducted by National Institute of Technical Teachers Training and Research, Chandirah.	7/1/2019	11/01/2019	Dr.A.D.Shinde College of Engineering, Badagaon.
02	“AICTE Sponsored STTP-3 on Battery Management and Control Techniques in EVs”	14/12/2021	19/12/2021	HIT Nidasoshi.

15. AWARDS, FELLOWSHIPS & OTHER DISTINCTIONS:

- Nil

16. ANY OTHER SIGNIFICANT INFORMATION:

- Nil

Smt.Purnima Savadatti